

Notice of References Cited			Application No.	Applicant(s)			
			09 039,106	Rozman			
Examiner		Group Art Unit		Page <u>1</u> of <u>1</u>			
U.S. PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	NAME		CLASS	SUBCLASS
*	A	5,303,138	4/94	Rozman		363	21
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						
	L						
	M						
FOREIGN PATENT DOCUMENTS							
*		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
	N						
	O						
	P						
	Q						
	R						
	S						
	T						
NON-PATENT DOCUMENTS							
*		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)				DATE	
*	U	"Current Controlled Synchronous Rectification", by B. Acker, C. C.R. Sullivan, S.R. Sanders, IEEE pp 185-191, May 94					
*	V	"High Efficiency DC-DC Converter" by I.D. Jitaru, G. Cucina, IEEE, pp. 638-644 May 1994					
	W						
	X						

* A copy of this reference is not being furnished with this Office action.
(See Manual of Patent Examining Procedure, Section 707.05(a).)